



# Architectural Opportunities and Challenges from Emerging Photonics in Future Systems

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- Specialization in future HPC and datacenter systems will stress the network
- Optical advancements bring significant promise but not as simple drop-in replacements of existing ones
  - Node and system reconfigurability
- \* Challenges remain
  - Including simulating optics devices at a system scale



## **HPC System Trends**



- \* Summit supercomputer at ORNL
  - Top performance in Linpack (top500.org results) with 122.3
     PetaFLOPS
  - 13MW > 13.9 GFLOPs / Watt

6 GPUs per node. 2 CPUs

- Next challenge: Exascale computing within 20MW
  - 50 GLOPs / Watt



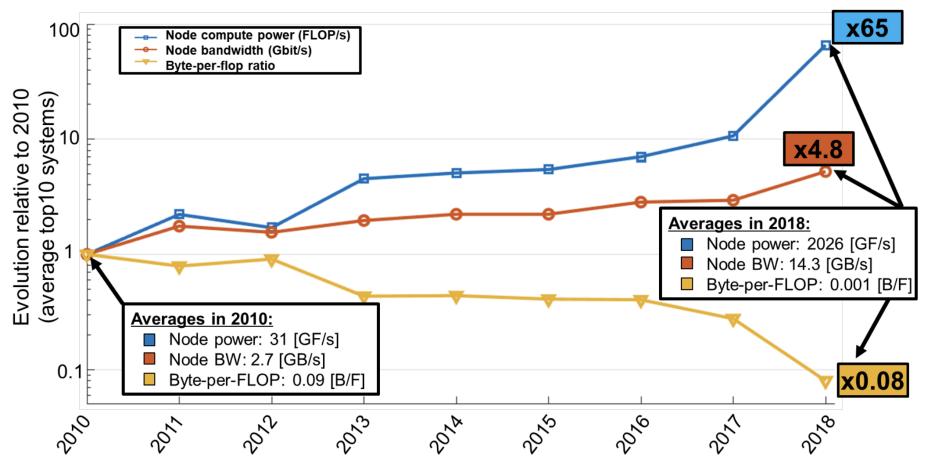




**Top 10 System Trends** 



### Performance/Communications Trends for Top 10 (2010-2018)



Sunway TaihuLight (Nov 2017) B/F = 0.004; Summit HPC (June 2018) B/F = 0.0005 → 8X decrease

Keren Bergman, "Empowering Flexible and Scalable High Performance Architectures with Embedded Photonics", IPDPS 2018





- ★ 14 GFLOPs / Watt (Summit) → 72 pJ / FLOP
   Image: 0.36 pJ / bit
- Exascale target: 50 GLOPs / Watt 20 pJ / FLOP
  - 0.1 pJ / bit

## **\*** Total budget

★ The above assume 200 bits / FLOP

Data Movement Energy:	
– Access SRAM	O(10fJ/bit)
– Access DRAM cell	O(1 pJ/bit)
– Movement to HBM/MCDRAM (few mm)	O(10 pJ/bit)
<ul> <li>Movement to DDR3 off-chip (few cm)</li> </ul>	O(100 pJ/bit)

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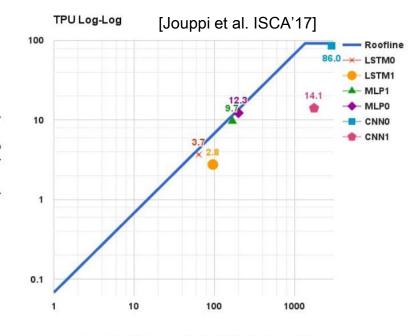


## Specialization May Be Limited By IO Google's TPU as an Example



- Dedicated hardware for DNNs
  - Peak compute capacity:92 TOPS/s (8-bit precision)
  - Peak bandwidth: 34 GB/s
- Must reuse a byte 2706 times to fully exploit compute capacity
  - Operational intensity: 2.7KOPs/byte, hit rate: 99.96%, 0.003 bit/OP
- Only two operations have high operational intensity: CNN0 and CNN1
- Operational intensity of others (e.g., translate and Rankbrain which are 90% of the applications) are 1 – 1.5 orders of magnitude smaller
- LSTM0 would require 40x more bandwidth to (theoretically) allow full TPU utilization

[Google cloud]

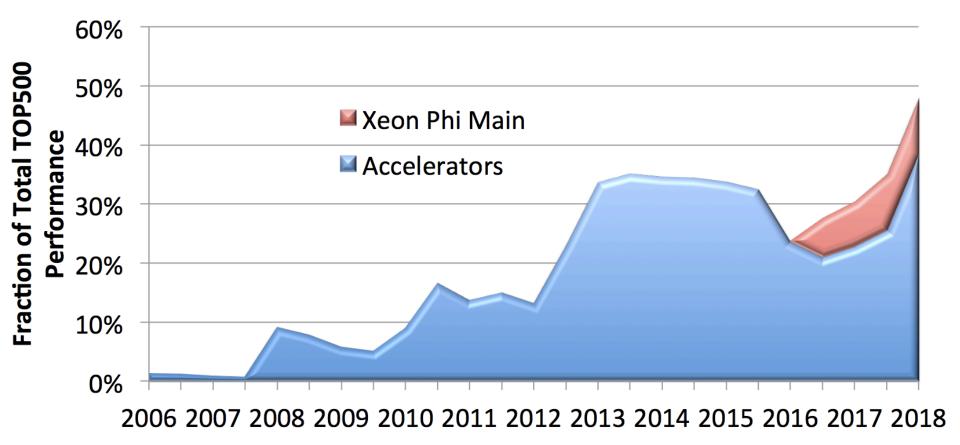


Operational Intensity: Ops/weight byte (log scale)

[Keren Bergman]





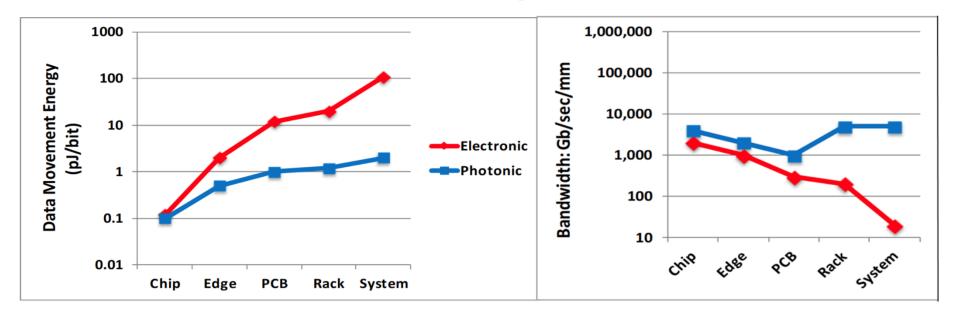








### **The Photonic Opportunity for Data Movement**



#### **Reduce Energy Consumption**

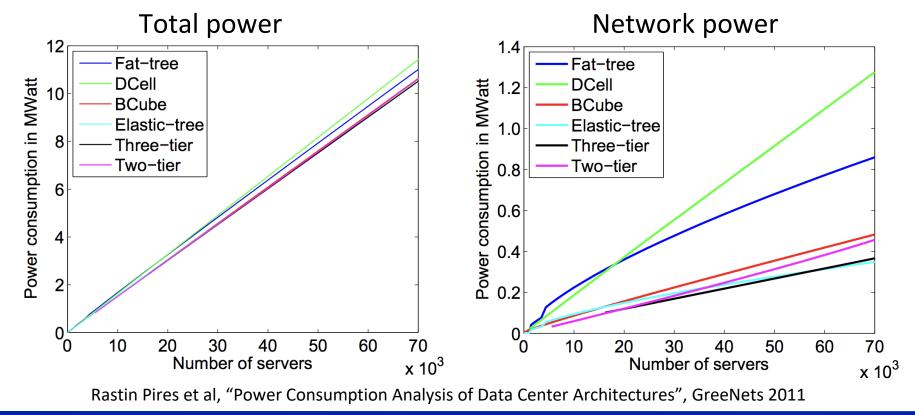
#### Eliminate Bandwidth Taper

R. Lucas et al., "Top ten exascale research challenges," DOE ASCAC subcommittee Report, 2014





- \* Even if we have a network that consumes no energy, we cannot reach a 2x improvement
  - Only 4% to 12% of total power is in the network
- \* Key: use emerging photonic components to change the architecture





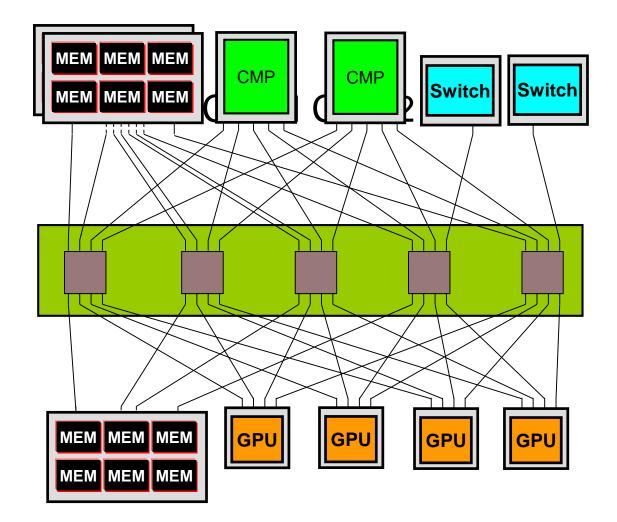


### ★ Intra node

- Resource disaggregation
- \* System-wide
  - Bandwidth steering





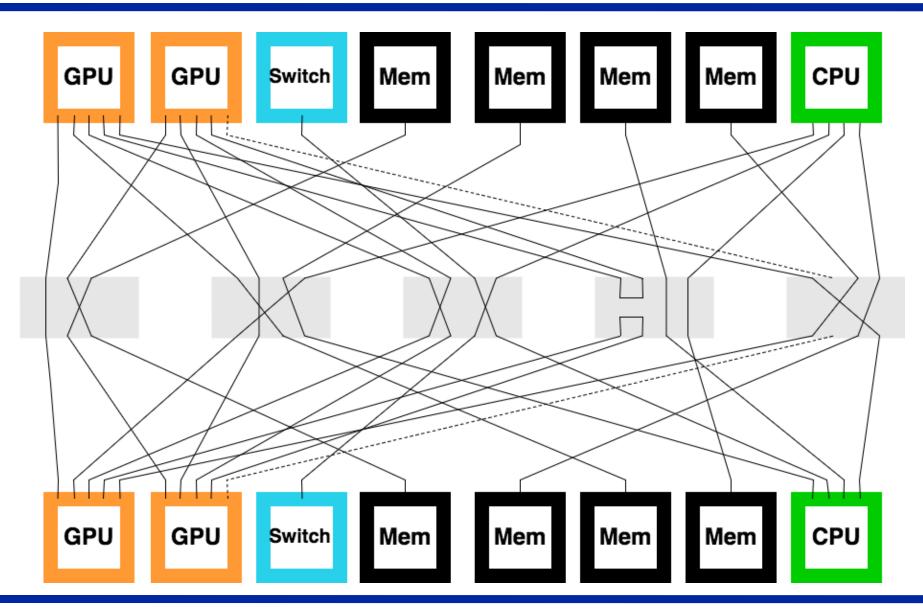


Keren Bergman, "PINE: An Energy Efficient Flexibly Interconnected Photonic Data Center Architecture for Extreme Scalability", OI 2018



## Intra-Node Reconfigurability

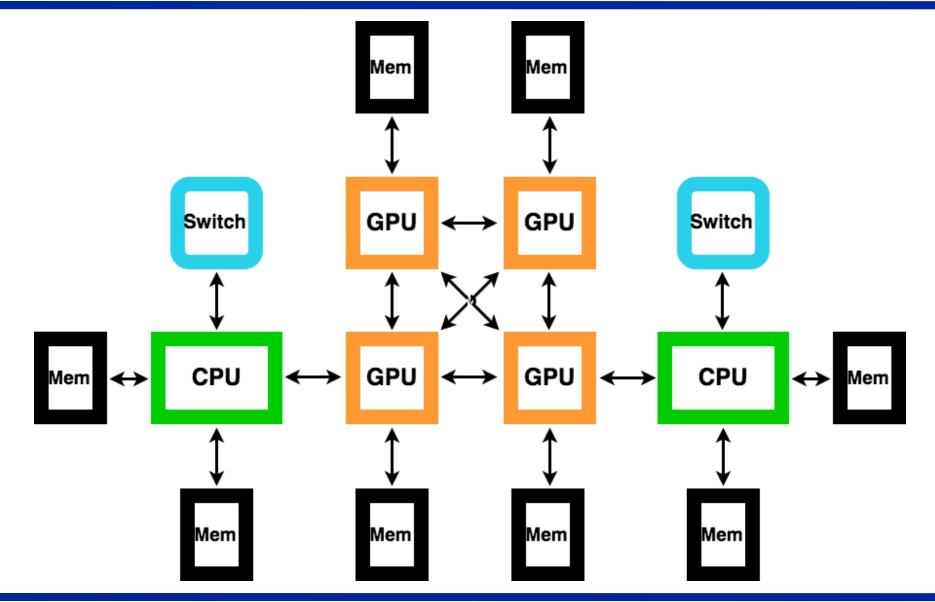






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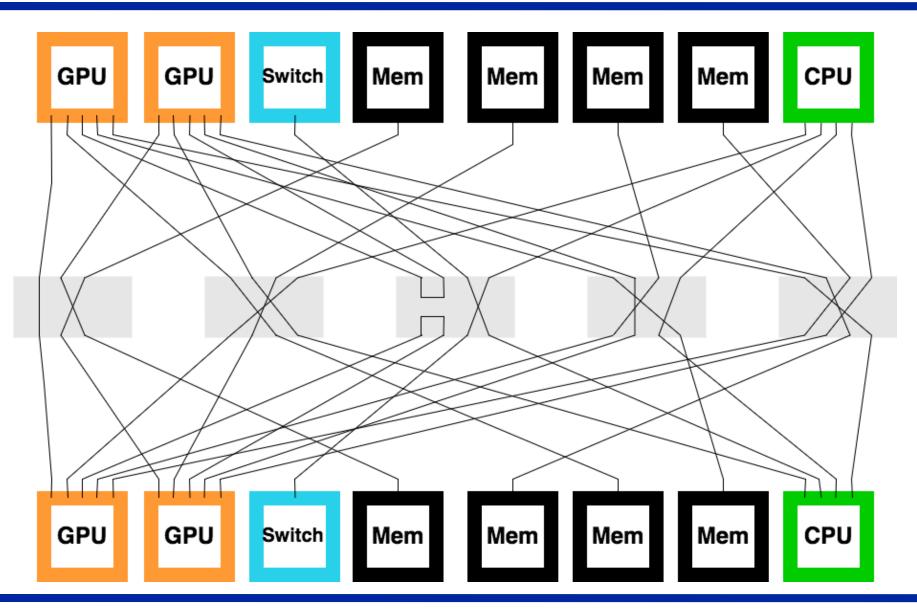






## Intra-Node Reconfigurability

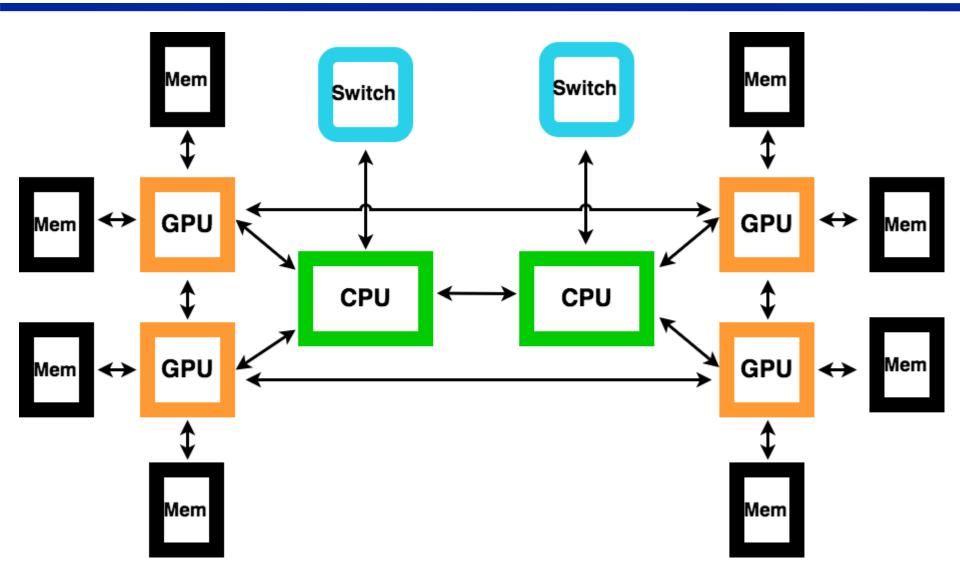






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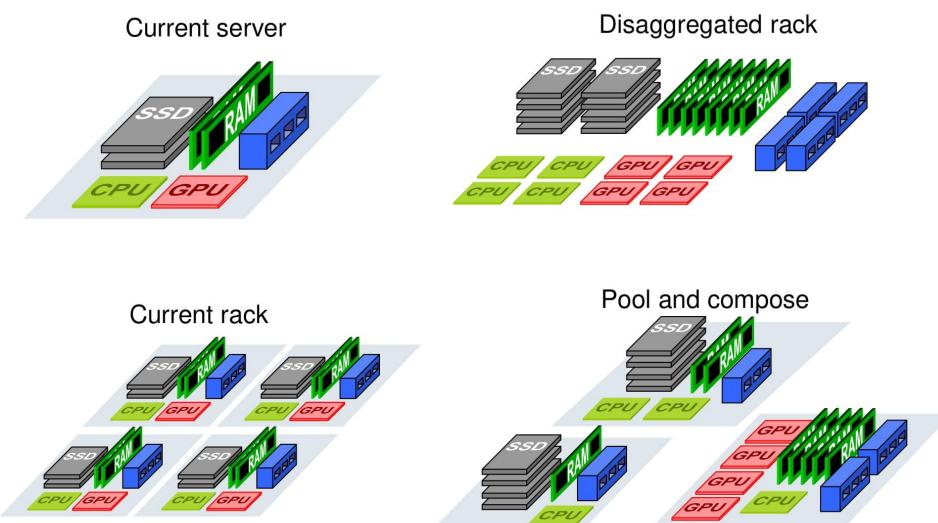






## **Aggregate Remote Resources**





Keren Bergman, "PINE: An Energy Efficient Flexibly Interconnected Photonic Data Center Architecture for Extreme Scalability", OI 2018



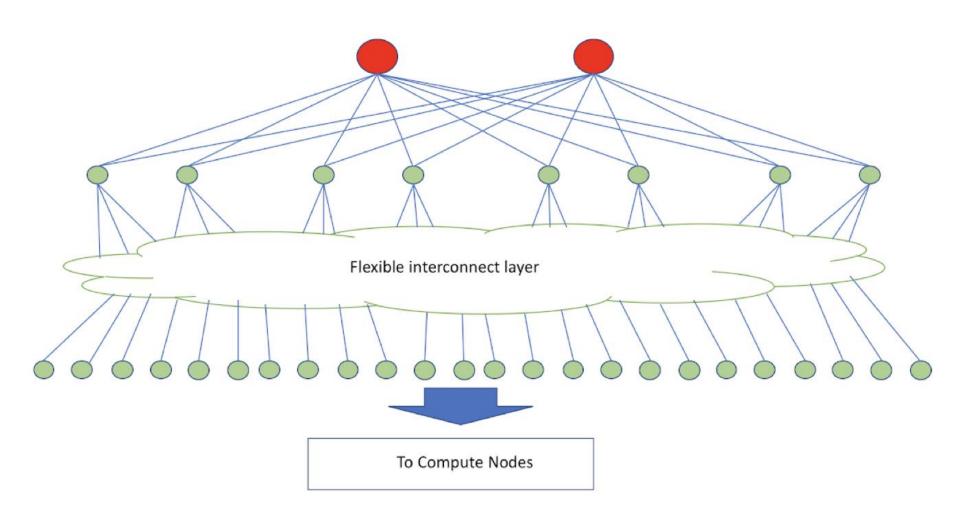


- \* Photonic switches with sufficient radix
- Efficient conversion to optics
   In package?
- How changing node configuration affects network traffic, scheduling, and system management [1]

[1] D. Z. Tootaghaj et al., "Evaluating the combined impact of node architecture and cloud workload characteristics on network traffic and performance/cost,",2015 IEEE International Symposium on Workload Characterization.







[Min Yee (Jason) The]

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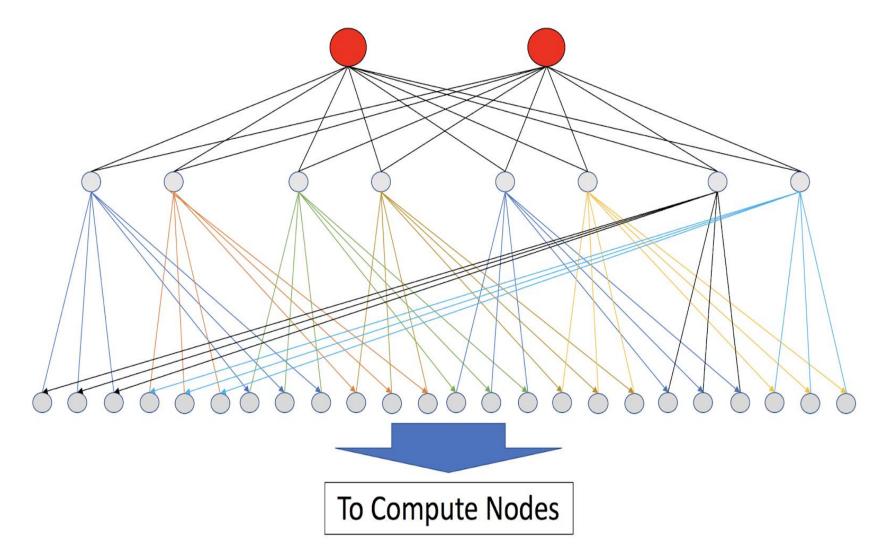
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[Min Yee (Jason) The]



**Bandwidth Steered** 





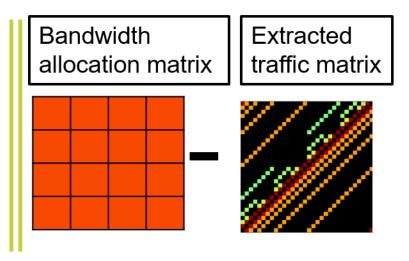
[Min Yee (Jason) The]



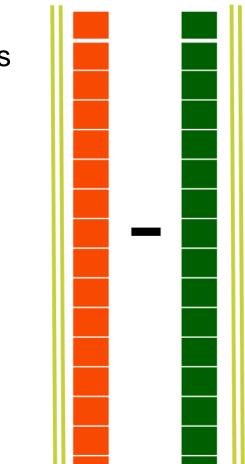
Transform into vector



- \* NP-hard optimally
- Respect physical limitations
- Understand implications in pathological cases
- \* Solid models of underlying optics technology
  - Cost of reconfiguration



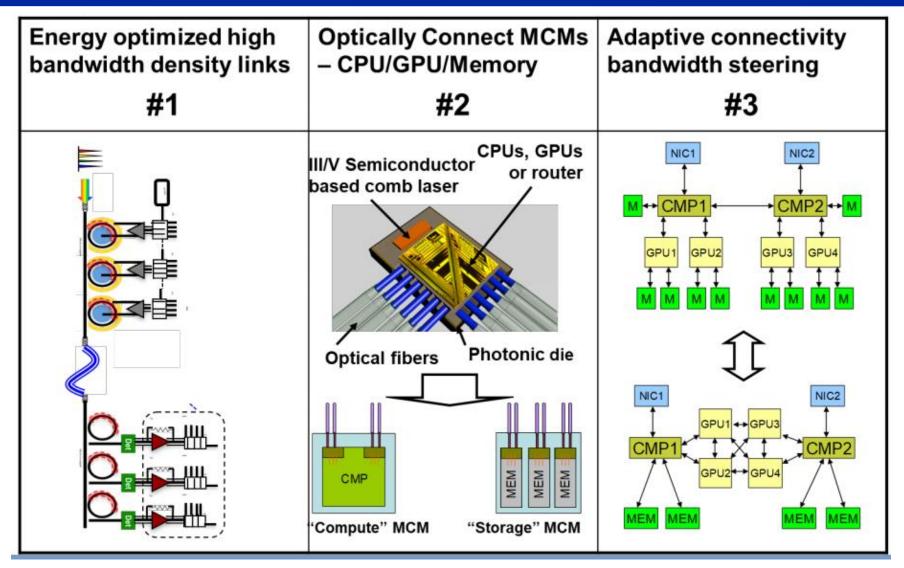
Transform matrix form into vector form



[Min Yee (Jason) The]





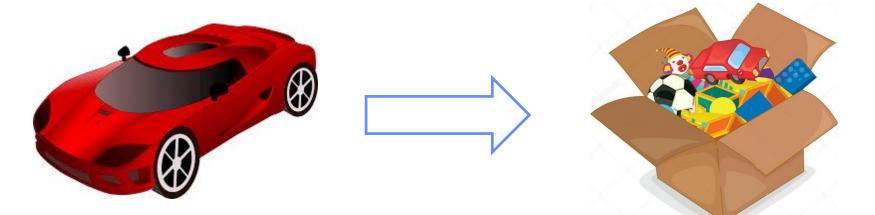


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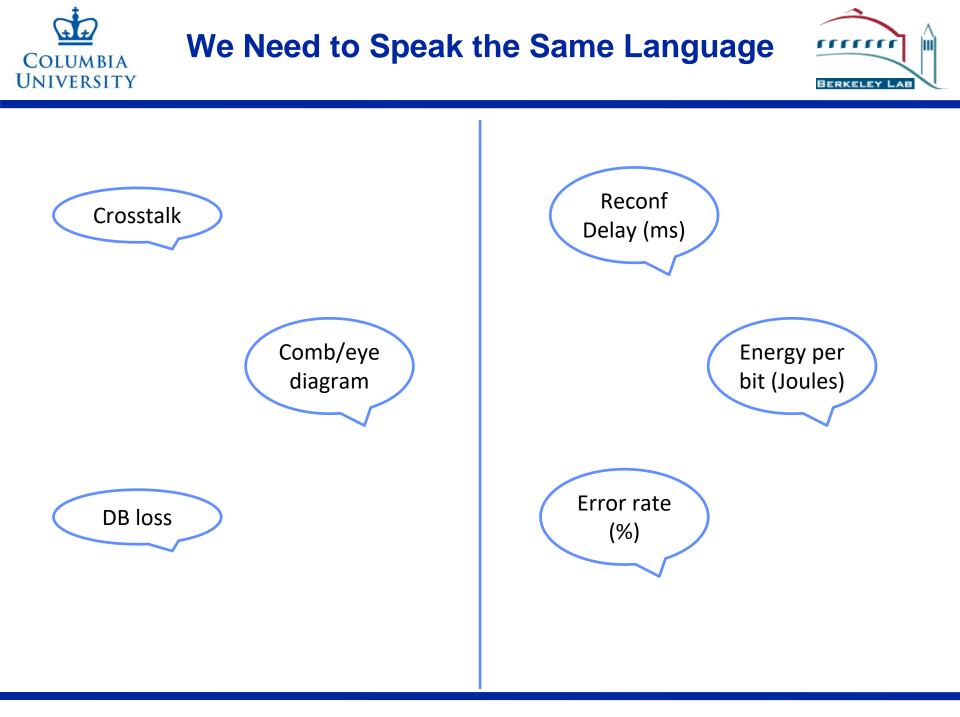


\* As an architect, new optical components are new toys that I add to my collection



 But in order for me to use this cool new toy, I need a user manual in a language I can understand

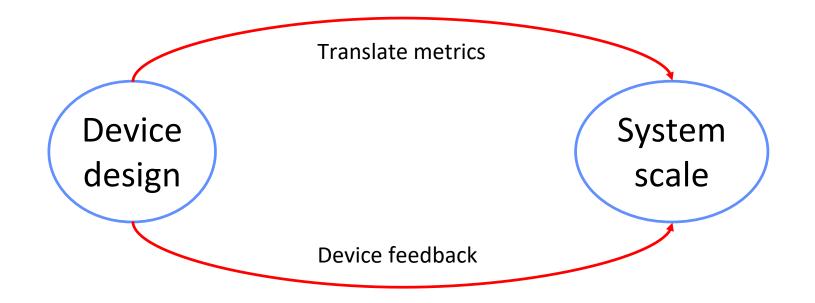








- \* Translate low-level metrics to architectural-level metrics
- \* Provide feedback to optimize devices for high-level impact
  - Prioritize reconfiguration time or energy?
  - Realize overhead of some choices, e.g., error rate
  - "Knobs" in the models are encouraged







- Reconfigurability relies on predicting, monitoring or exposing application demands
  - Weigh the cost of reconfiguration
- How to use consecutive switch hops in the optical domain
   Without conversion to electrical
- Faster reconfiguration and fast turn off/on lasers may change network design significantly





- \* "Electronics are approaching their limit"
  - Optics will replace electronics"
- \* Electronics are fundamentally good at some aspects
  - E.g., computing such as for routing and reconfigurability
  - Packet switching -> higher utilization (dynamic traffic)
- **\*** Two options:
  - Give up electronics entirely and drastically re-design our networks with possible important drawbacks
     With no overdesigning
  - Networks with both electronics and photonics
     We just have to figure out exactly how much of each





 Specialization in future HPC and datacenter systems will stress the network

**Questions?** 

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  - Node and system reconfigurability
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